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THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: JOSEPH HUANG ET AL.

Filed:

JANUARY 15, 2004

For:

APPARATUS AND METHODS FOR PROVIDING REDUNDANCY IN

INTEGRATED CIRCUITS

Serial No.:

10/757,928

Group Art Unit:

2819

Examiner:

UNKNOWN

Atty Dkt:

ALTR:023

Pursuant to 37 C.F.R. 1.8, I certify that this correspondence is being deposited with the U.S. Postal Service in a first class, postage prepaid envelope addressed to: Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313 on the date below:

Date

Name

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313

Sir:

Pursuant to 37 C.F.R. §§ 1.56, 1.97, and 1.98, it is respectfully requested that this Information Disclosure Statement be entered and the document(s) listed on attached Form PTO-1449 be considered by the Examiner and made of record.

In accordance with 37 C.F.R §§ 1.97(g),(h), this Information Disclosure Statement is not to be construed as a representation that a search has been made, and is not to be construed to be an admission that (a) the information cited is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b); and/or (b) that the information cited is, or is considered to be prior art.

The present Information and Disclosure Statement is being filed prior to the receipt of a first Official Action reflecting an examination on the merits, and hence is believed to be timely filed in accordance with 37 C.F.R. § 1.97(b). No fees are believed to be due in connection with the filing of this Information Disclosure Statement, however, should any fees under 37 C.F.R. §§ 1.16 to 1.21 be deemed necessary for any reason relating to these materials, the Commissioner is hereby authorized to deduct said fees from Deposit Account No. 10-1205/ALTR:023.

A copy of the listed document(s) required by 37 C.F.R. § 1.98(a)(2) is enclosed for the convenience of the Examiner.

Applicant respectfully requests that the listed document(s) be made of record in the present case.

Respectfully submitted,

Maximilian R. Peterson

Reg. No. 46,469

Attorney for Applicant

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FAX: (512) 347-1615

**Enclosures** 

## Form PTO-1449 (modified)

List of Patents and Publications for Applicant's

Atty. Docket No. ALTR:023

Serial No. 10/757,928

Applicant HUANG ET AL.

#### INFORMATION DISCLOSURE STATEMENT

(Use several sheets if necessary)

Filing Date: 1/15/04

Group: 2819

J.S. Patent Documents See Page 1

Foreign Patent Documents
See Page

Other Art See Page 2

#### **U.S. Patent Documents**

			U.S. Pa	tent Docume	ents		
Exam. Init.	Ref. Des.	Document Number	Date	Name	Class	Sub Class	Filing Date if App.
	A1	4,281,398	7/28/81	McKenny et al.			2/12/80
	A2	US2002/01058 54A1	8/8/02	Huh et al.			1/23/02
	A3	US2003/02064 77A1	11/6/03	Waller et al.			5/22/03
	A4	US2004/00221 · 10A1	2/5/04	Haraguchi et al.			2/25/03
	A5	US2004/00278 93A1	2/12/04	Kurita			7/14/03
	A6	4,724,422	2/9/88	Golab			9/13/85
	A7	4,829,481	5/9/89	Johnson et al.			10/22/87
	A8	4,887,239	12/12/89	Turner			7/7/86
	A9	5,349,555	9/20/94	Nakajima			4/27/93
	A10	5,821,770	10/13/98	Rees			4/30/96
	A11	5,825,698	10/20/98	Kim et al.			10/30/97
	A12	6,020,777	2/1/00	Bracchitta et al.			9/26/97
	A13	6,205,050	3/20/01	Tamaki			12/9/99
	A14	6,215,173	4/10/01	Echigoya			11/12/99.
	A15	6,215,715	4/10/01	Lee et al.			7/1/99
	A16	6,236,241	5/22/01	Liu et al.			4/12/00
	A17	6,404,264	6/11/02	Daniel et al.			12/6/99
-	A18	6,600,686	7/29/03	Huh et al.			. 1/23/02
	A19	6,281,739	8/28/01	Matsui			7/6/99

Exa	mi	ne	T:

Date Considered:

EXAMINER: initial if reference considered, whether or not citation is in conformance with MPEP609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Form PTO-1449 (modified)		Atty. Docket No. ALTR:023	Serial No. 10/757,928	
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U.S. Patent Documents		atent Documents	Other Art	
See Page 1		See Page	See Page 2	

### **Foreign Patent Documents**

Foreign Patent Documents							
Exam. Init.	Ref. Des.	Document Number	Date	Country	Class	Sub Class	Translation Yes/No
	Bi						

# Other Art (Including Author, Title, Date, Pertinent Pages, Etc.)

Other Art (Including Author, Title, Date, Pertinent Pages, Etc.)					
Exam. Init.	Ref. Des.	Citation			
	C1	Ouellette et al., "Shared Fuse Macro For Multiple Embedded Memory Devices With Redundancy", IEEE Custom Integrated Circuits Conference, 2001, pps. 191-194.			
	C2	McCollum et al., "Reliability Of Antifuse-Based Field Programmable Gate Arrays For Military And Aerospace Applications", Actel Corporation, Abstract, September 2001, 6 pgs.			
	C3	Benedetto et al., "Amorphous Silicon Antifuse Programmable-Array-Logic Devices For High Reliability Space Applications", UTMC Microelectronic Systems, January 2002, 4 pgs.			
	C4	Laville et al., "Integrated Offset Trimming Technique", 27 <sup>th</sup> European Solid-State Circuits Conference, September 2001, 4 pgs.			
	C5	Actel, Reliability Considerations For Automotive FPGAs, White Paper, September 2003, 17 pgs.			
	C6	MRAM Redundancy Scheme (prior art available prior to filing date of the present application)			